

[illegible]

## Abstract of the Disclosure

A system provides communication between a plurality of cores in an integrated circuit. The system comprises a circular segmented bus operatively connected to each of the cores for transferring data between the plurality of cores. An arbiter arbitrates which of the plurality of cores can transmit data at any given time.

## Figures

Figure 1: A vertical list of text elements, possibly a table of contents or a list of items, arranged in a single column. The text is small and difficult to read, but appears to be organized in a structured manner.